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BCA-202(N)

B. C. A. (Second Semester) EXAMINATION, May, 2013

(New Course)

Paper Second

DIGITAL ELECTRONICS AND COMPUTER ORGANIZATION

Time: Three Hours]

Maximum Marks: 75

Note: Section A is compulsory. Attempt seven questions out of ten questions from Section B and one question from Section C.

Section-A

- (a) If 73_x (in base-x number system) is equal to 54_y (in base y number system), the possible values of x and y are:
 - (i) 8, 16
 - (ii) 10, 12
 - (iii) 9, 13
 - (iv) 8, 13
 - (b) Implement EX-OR Gate with the help of NAND Gate. Show the output at each step.
- Draw the logic diagram for BCD to Excess-3 code converter. Discuss each step through truth table and K-map.

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P. T. O.

Section-B

Draw the block diagram of digital computer and explain each unit.

Two digital signals x and y are as follows:

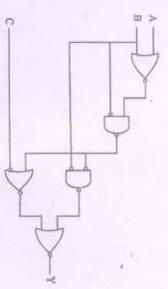
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Find AND, OR, NOT, EX-OR, EX-NOR operations.

- (a) What is Cache memory? What is the need of Cache memory in computer system?
- 0 provide a memory capacity of 256 K bytes? How many 32 K × 1 RAM chips are needed to
- 6 (a) Express the Boolean function F = X + Y'Z in a sum
- 0 product of maxterm form. Express the Boolean function F = AB + A'D in a
- 7. (a) Implement a full-adder circuit.
- 0 Discuss Half substractor and draw the boolean function for the two outputs.
- 2 x 4 decoder. Construct a 5 x 32 decoder with four 3 x 8 decoder and a
- 9 What is Flip-Flop? Discuss J-K flip-flop in detail
- 10. Draw the block diagram of 4 bit register with parallel load

For the logic circuit shown in the figure, Boolean expression for the output Y is: the simplified



Simplify the following boolean function through K-map: 6

 $F(w, x, y, z) = \Sigma (1, 3, 4, 6, 9, 11, 12, 14)$

Section-C

13. (a) Simplify the boolean function:

0

x'y' + xy + x'y

E AB + ABC + A (B + A B)

(E) Find the dual of :

F = A'BC' + A'B'C

- (9) 8 What is the maximum number of different boolean function involving n Boolean variable.
- The hexadecimal representation of (657)8-
- 0 Simplify using K-map:

 $F(w, x, y, z) = \Sigma(0, 4, 5, 7, 8, 9, 13,$ 15)

- 14. (a) 0, 1, 2, 3, 4, 5, 6, 7 and repeat using T flip-flops. Design a counter with the following binary sequence IO
- 0 Draw the block diagram of 4 bit full adders circuit. 5

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